

# Ph.D. Diagnostic and Comprehensive Examination Spring 2022

Last updated on 2/14/2022

## Examination Guidelines

- The examination is “closed-book” and no formula sheet is allowed. Some questions include reference formulas. A scientific calculator is allowed.
- This is a four-hour examination.
- M.E. students need to answer five questions, but no more than two from the mathematics group. The ME students’ responses are graded at a master’s level.
- Ph.D. students need to answer eight questions, but no more than three from the mathematics group. The Ph.D. students’ responses are graded at a doctoral level.
- The answers to only five (M.E.) or eight (Ph.D.) questions need to be turned in separate blue books.
- Students need to abide by Old Dominion University’s honor pledge. No material shall be shared without prior permission of the proctor(s).
- Copies of sample examinations are available at <https://www.odu.edu/ece/students/graduate>

## Examination Topics

Problem	Topic	Suggested Text and Chapters or Topics	Primary Faculty Member(s)
A1	<b>MATH</b> Complex Variables and Differential Equations	Complex functions, analyticity & the Cauchy-Riemann equations, contour integration & the residue theorem, linear differential equations with constant coefficients, integrating factors, initial-value problems, method of undetermined coefficients, power series solutions. <ul style="list-style-type: none"> <li>• “Complex Variables and Applications,” 3rd Edition, J. W. Brown and R. V. Churchill, McGraw- Hill, 1995, Chapters 1–7.</li> <li>• “Elementary Differential Equations and Boundary Value Problems” 9th ed., W. E. Boyce and R. C. DiPrima, Wiley, 2008, Chapters 1–6.</li> </ul>	Dr. Xiao
A2	<b>MATH</b> Vector Calculus	“Advanced Engineering Mathematics,” E. Kreyszig, 10 <sup>th</sup> ed., Wiley 2011, Chapters 9–10	Dr. Vahala
A3	<b>MATH</b> Linear Algebra	<ul style="list-style-type: none"> <li>• “Linear Algebra with Applications,” G. Williams, Jones and Bartlett Publishers 2010. Chapters 1–5.</li> <li>• “Linear Algebra and Its Applications,” G. Strang, 4th edition, Brooks/Cole Publishing 2006, Chapters 1–6.</li> </ul>	Dr. Popescu
A4	<b>MATH</b> Probability	“Probability and Statistics” A. Papoulis, Prentice Hall, 1990, Chapters 1–6, 8, 9.	Dr. Gray

<b>CIRCUITS &amp; ELECTRONICS</b>			
<b>B1</b>	<b>CIRCUITS</b> Sinusoidal Steady State Analysis	“Electric Circuits,” J. W. Nilsson & Susan A. Riedel, 9 <sup>th</sup> ed., Prentice Hall, Chapters 7 – 10.	Dr. Lakdawala
<b>B2</b>	<b>CIRCUITS</b> Circuit Analysis with the Laplace Transform	“Electric Circuits,” J. W. Nilsson & Susan A. Riedel, 9 <sup>th</sup> ed., Prentice Hall, Chapter 13.	Dr. Lakdawala
<b>B3</b>	<b>ELECTRONICS</b>	“Microelectronic Circuits,” A. S. Sedra and K. C. Smith, 5 <sup>th</sup> ed., Oxford Univ. Press, New York, 1998. Chapters: 2-5.	Dr. Namkoong
<b>SYSTEMS, SIGNAL AND IMAGE PROCESSING</b>			
<b>C1</b>	<b>IMAGE PROCESSING</b>	“Digital Image Processing,” R. C. Gonzalez and R. E. Woods, 3 <sup>rd</sup> ed., Prentice Hall, 2007, Chapters 1 – 4.	Dr. Chen
<b>C2</b>	<b>DIGITAL SIGNAL PROCESSING</b> Discrete-Time System Analysis	“Linear Systems and Signals,” B. P. Lathi, 2 <sup>nd</sup> ed., Oxford, 2005, Chapters 3, 5.	Dr. Li
<b>C3</b>	<b>DIGITAL SIGNAL PROCESSING</b> Sampling and Fourier Analysis of Discrete-Time Signals and Systems	“Linear Systems and Signals,” B. P. Lathi, 2 <sup>nd</sup> ed., Oxford, 2005, Chapters 8, 9.	Dr. Li
<b>C4</b>	<b>CONTROL SYSTEMS</b>	“Control Systems Engineering,” N. S. Nise, 6 <sup>th</sup> ed., Wiley, 2011, Chapters 2 – 11, Secs. 12.1 – 12.2.	Dr. González
<b>C5</b>	<b>COMMUNICATION SYSTEMS</b>	“Fundamentals of Communication Systems,” J. G. Proakis and M. Salehi, Pearson/Prentice-Hall, 2005. Chapters 1 – 7.	Dr. Popescu
<b>C6</b>	<b>COMMUNICATION NETWORKS</b>	<ul style="list-style-type: none"> <li>▪ Data Link Layer error detection and correction methods</li> <li>▪ Sliding window protocols</li> <li>▪ Multiple access protocols (Aloha variants, CSMA with CD/CA)</li> <li>▪ Routing algorithms (Link State, Distance Vector, RIP, OSPF)</li> <li>▪ TCP congestion control</li> </ul> <ul style="list-style-type: none"> <li>• “Computer Networks,” A. S. Tanenbaum, Prentice Hall, 5th Ed., 2011, Sections 3.1 – 3.4, 4.1 – 4.2, 5.1 – 5.6, 6.2, 6.4, 6.5.</li> <li>• “Computer Networking: A Top-Down Approach,” J. F. Kurose and K. W. Ross, 5th ed., 2010 Chapters 1, 3–5.</li> </ul>	Dr. Xin

<b>PHYSICAL ELECTRONICS I</b>			
<b>D1</b>	<b>ELECTROMAGNETICS</b> Maxwell Equations, Propagation, Reflection and Transmission of Plane waves	“Applied Electromagnetism,” L. C. Shen and J. A. Kong, 3 <sup>rd</sup> ed., Cengage Learning, Chapters 2 – 4.	Dr. Jiang
<b>D2</b>	<b>ELECTROMAGNETICS</b> Electrostatics	“Applied Electromagnetism,” L. C. Shen and J. A. Kong, 3 <sup>rd</sup> ed., PWS Foundation Engineering Series, Chapters 9 – 10.	Dr. Namkoong
<b>D3</b>	<b>OPTICAL FIBER COMMUNICATIONS</b>	<ul style="list-style-type: none"> <li>• “Optoelectronics,” Wilson &amp; Hawks, Prentice Hall, 3<sup>rd</sup> ed., 1998, Chapters 4, 7, and 8.</li> <li>• “Optical Fiber Communication,” McGraw Hill, 4<sup>th</sup> ed., 2011, Chapters 1 – 4, 6 &amp; 7, 11.</li> </ul>	Dr. Laroussi
<b>PHYSICAL ELECTRONICS II</b>			
<b>E1</b>	<b>SOLID STATE ELECTRONICS</b>	“Semiconductor Devices,” S. M. Sze, Wiley, 2 <sup>nd</sup> edition 2001, Chapters 4 – 9.	Dr. Baumgart
<b>E2</b>	<b>PHYSICAL ELECTRONICS</b>	“Semiconductor Devices,” S. M. Sze, Wiley, 2 <sup>nd</sup> edition 2001, Chapters 1 – 4.	Dr. Marsillac
<b>E3</b>	<b>PLASMA SCIENCE AND DISCHARGES</b>	<p>Maxwell-Boltzmann distribution, plasma frequency, Debye-shielding, drift, diffusion, plasma conductivity, waves in plasmas with no B field, reaction rates, particledynamics.</p> <ul style="list-style-type: none"> <li>• “Introduction to Plasma Physics,” F. F. Chen, Plenum Press, 1974.</li> <li>• “Principles of Plasma Discharges and Materials Processing,” M. A. Lieberman and A. J. Lichtenberg, 2<sup>nd</sup> ed., Chapters 2 – 6, 14.</li> </ul>	Dr. Elsayed-Ali

<b>COMPUTER SYSTEMS</b>			
<b>F1</b>	<b>MICROPROCESSORS</b>	<ul style="list-style-type: none"> <li>• Ch. 1. Microprocessor systems, microcontrollers and integrated peripherals.</li> <li>• Ch. 2. Programming microprocessors, assembly language programming, programmer's model, instruction set architecture, addressing modes, structured programming and pseudocode.</li> <li>• Ch. 3. Assembly language parameter passing, using the stack and local variables, subroutines.</li> <li>• Ch. 4. Microprocessor interfacing, dealing with timing problems, assembly coding for speed, pulse- width modulation.</li> <li>• Ch. 5. Memories in microprocessor systems, program and data memory, efficient assembly coding for small memories.</li> <li>• Ch. 6. Interrupts, exception handling, real-time processing.</li> <li>• "Microprocessor Systems Design: 68000 Hardware, Software, and Interfacing," A. Clements, PWS Publishing Company, 1997.</li> </ul>	Dr. Belfore
<b>F2</b>	<b>DIGITAL SYSTEM DESIGN</b>	<ul style="list-style-type: none"> <li>• "The designer's guide to VHDL," P. Ashenden, Morgan-Kaufman, 3rd ed., 2008. (VHDL reference)</li> <li>• "VHDL &amp; Computer Design Fundamentals," M. Mand &amp; C. Kime, 4th ed., Prentice Hall, 2008, Chapters 1–9.</li> <li>• "Digital Design Using VHDL," C. H. Roth and L.K. John, 2nd ed., Cengage Learning, 2007, Chapters 1–5, 8, 9.</li> </ul>	Dr. Belfore
<b>F3</b>	<b>COMPUTER ARCHITECTURE</b>	<ul style="list-style-type: none"> <li>• Ch. 2: Architecture Classification, Instruction Set Architecture</li> <li>• Ch. 3: Number systems and arithmetic, IEEE (standard 754) floating point arithmetic.</li> <li>• Ch. 4: Datapath and controller design, Pipelining - design, hazards, dependency resolution schemes</li> <li>• Ch. 5: Memory system design, MMUs, caches and hierarchies, replacement policies.</li> </ul>	Dr. Chen
<b>F4</b>	<b>ALGORITHMS</b>	"Introduction to Algorithms," T. H. Cormen, C. E. Leiserson, R. L. Rivest and C. Stein, 3 <sup>rd</sup> ed., MIT Press, 2009, Chapters 2 – 4, 7, 9.	Dr. Belfore
<b>F5</b>	<b>DATA STRUCTURES</b> Stacks, queues, and linked lists. Binary trees.	<ul style="list-style-type: none"> <li>• Comparison of elementary data structures such as stacks, queues, and linked lists</li> </ul> <p>"Data Structures with C++ Using STL," W. Ford and W. Topp, 2<sup>nd</sup> ed., Prentice Hall, 2002, Chapters 5 – 13.</p>	Dr. Chen
<b>F6</b>	<b>LOGIC/DIGITAL CIRCUITS</b>	Digital Design and Computer Architecture, Second Edition, 2012, by David Harris and Sarah Harris	Dr. Al-Assadi

<b>CYBERSECURITY</b>			
<b>G1</b>	<b>COMPUTER NETWORKS AND SECURITY</b>	“Computer Networking: A Top-Down Approach,” J. F. Kurose and K. W. Ross, 8th ed., 2021, Chapters 7 and 8	Dr. Alsharif
<b>G2</b>	<b>CYBER DEFENSE FUNDAMENTALS</b>	“Introduction to Cryptography with Coding Theory”, Wade Trappe and Lawrence C. Washington, Chapter 1-9	Dr. Wu
<b>G3</b>	<b>CYBER PHYSICAL SYSTEM SECURITY</b>	“Security in Computing”, 5th edition, by Charles Pfleeger, Shari Lawrence Pfleeger, and Jonathan Margulies, Chapter 3-9	Dr. Wu
<b>G4</b>	<b>FOUNDATIONS OF CYBERSECURITY</b>	Set-UID Programs, Buffer Overflow Attack and Format String Vulnerability.  “Computer Security- A Hands-on Approach”, Wenliang Du, 1 <sup>st</sup> Edition, Chapters 1, 4 and 6	Dr. Shetty
<b>G5</b>	<b>SECURITY AND PRIVACY OF EMBEDDED SYSTEMS</b>	E. A. Lee and S. A. Seshia, Introduction to Embedded Systems - A Cyber-Physical Systems Approach, Second Edition, by, MIT Press, 2017.  Link to download pdf: <a href="https://ptolemy.berkeley.edu/books/leeseshia/">https://ptolemy.berkeley.edu/books/leeseshia/</a>  Chapter 17: Security and Privacy	Dr. Al-Assadi